



Einladung zum Informatik-Kolloquium

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Formalizations of Logics and Algorithms

Abstract: We first give an overview of recent formalizations of logics and algorithms in the Isabelle proof assistant. We then consider the development of a verified miniature proof assistant inside Isabelle using the functional programming language Standard ML (Programming and Verifying a Declarative First-Order Prover, Special Issue on Automated Reasoning, AI Communications May 2018).

Short bio: Jørgen Villadsen is associate professor at DTU Compute, Department of Applied Mathematics and Computer Science, Technical University of Denmark. His research is in logic, computational linguistics and artificial intelligence / multi-agent systems. He is the head of study for the MSc in Computer Science and Engineering programme.

Dienstag, 19.06.2018, 16 Uhr s.t.

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